

SANYO Semiconductors DATA SHEET



Monolithic Digital IC LB1947 — PWM Current Control Type **Bidirectional Motor Driver**

Overview

The LB1947 is a PWM current control type bidirectional motor driver IC.

Features

- PWM current control (fixed OFF time)
- Selectable current decay pattern (FAST, SLOW, and MIX DECAY modes)
- Simultaneous ON prevention function (feed-through current prevention)
- Built-in thermal shutdown circuit
- Built-in noise canceler

Specifications Absolute Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum motor supply voltage	V _{BB} max		50	V
Output peak current	I _O peak	t _W ≤ 20µs	2.25	А
Output continuous current	I _O max		2.0	А
Logic supply voltage	V _{CC} max		7.0	V
Logic input voltage range	VIN		-0.3 to V _{CC}	V
Emitter output voltage	V _E max		1.1	V
Reference voltage	V _{REF}		-0.3 to V _{CC}	V
Allowable power dissipation	Pd max		1.6	W
Operating temperature	Topr		-20 to +85	°C
Storage temperature	Tstg		-55 to +150	°C

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Allowable Operating Ranges at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Motor supply voltage	V _{BB}		10 to 45	V
Logic supply voltage	V _{CC}		4.75 to 5.25	V
Reference voltage	V _{REF}		0 to V _{CC} -2	V

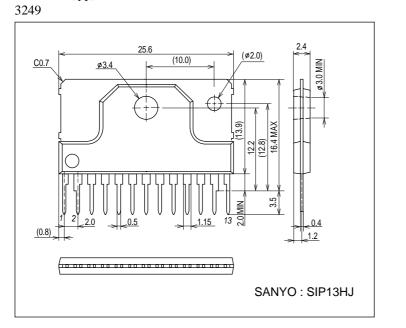
Electrical Characteristics at Ta = 25 °C, $V_{CC} = 5V$

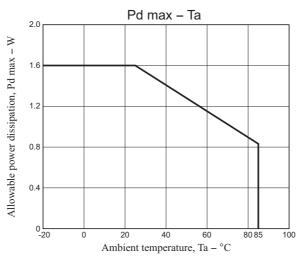
Parameter	Symbol Conditions		Ratings			Unit
T drameter			min	typ	max	
Output Block						
Output stage supply current	I _{BB} ON	No-load state, Forward	0.4	0.6	1.0	mA
	I _{BB} BR	No-load state, Brake	0.2	0.4	0.8	mA
	I _{BB} OFF	No-load state, Output off	0.2	0.4	0.8	mA
	I _{BB} wt	No-load state, Standby mode			0.1	mA
Output saturation voltage	V _O sat1	I _O = +1.0A, Sink		1.2	1.5	V
	V _O sat2	I _O = +2.0A, Sink		1.6	1.9	V
	V _O sat3	I _O = -1.0A, Source		1.8	2.2	V
	V _O sat4	I _O = -2.0A, Source		2.1	2.4	V
Output leak current	IO1(leak)	V _O = V _{BB} , Sink			50	μA
	I _O 2(leak)	V _O = 0V, Source	-50			μA
Output sustain voltage	VSUS	L = 3.9mH, I_O = 2.0A, Design guarantee value*	50			V
Logic Block						
Logic supply current	I _{CC} ON	IN1: High, IN2: Low, ST: High	11	16	21	mA
	I _{CC} BR	IN1: Low, IN2: High, ST: High		16	21	mA
	I _{CC} OFF	IN1: Low, IN2: Low, ST: High	11	16	21	mA
	ICC wt	ST: Low	1.0	2	3.0	mA
Input voltage	V _{IN} H		2			V
	VINL				0.8	V
Input current	I _{IN} H	V _{IN} = 5V	60	90	120	μA
	I _{IN} L	V _{IN} = 0.8V	6	10	13	μA
Sensing voltage V _E			0		1.1	V
Sensing voltage 25H	V _{EH} 25	VI = High, V _{REF} = 2.5V	0.970	1.0	1.030	V
Sensing voltage 25L	V _{EL} 25	VI = Low, V _{REF} = 2.5V	0.483	0.5	0.513	V
Sensing voltage 15H	V _{EH} 15	VI = High, V _{REF} = 1.5V	0.385	0.4	0.410	V
Sensing voltage 15L	V _{EL} 15	VI = Low, V _{REF} = 1.5V	0.190	0.2	0.210	V
Sensing voltage 05H	V _{EH} 05	VI = High, V _{REF} = 0.5V	0.190	0.2	0.210	V
Sensing voltage 05L	V _{EL} 05	$VI = Low, V_{REF} = 0.5V$	0.092	0.1	0.108	V
Reference current	Iref	V _{REF} = 1.0V	-0.5		+0.5	μA
CR pin current	ICR			-1.3	-1.04	mA
MD pin voltage	V _{MD} H		V _{CC} -0.3			V
	V _{MD} M		0.3V _{CC}		V _{CC} -1.0	V
	V _{MD} L				.0.4	V
MD input current	IMDH	MD = V _{CC} -0.5V, CR = 1.0V	-1.0		+1.0	μA
	IMDL	MD = 0.4V, CR = 2.0V	-5.0			μA
Thermal shutdown temperature	TSD	Design guarantee value*		170		℃

* Design guarantee value, Do not measurement.

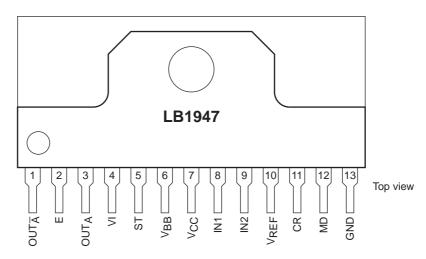
Package Dimensions

unit : mm (typ)

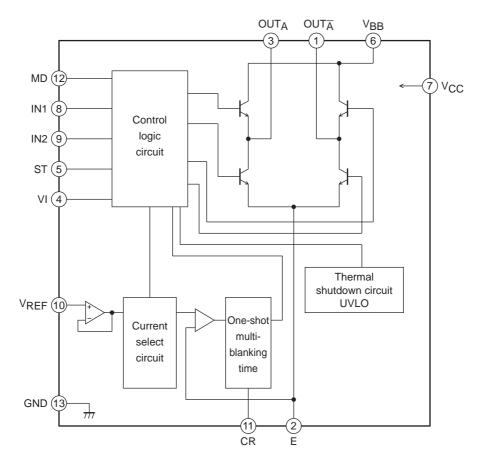








Block Diagram



Truth Table

ITUUT I	abic						
IN1	IN2	ST	VI	MD	OUTA	OUTA	Operating mode
н	L	Н	Н	L	Н	L	Forward, 2/5 times, FAST
Н	L	Н	Н	М	Н	L	Forward, 2/5 times, MIX
н	L	н	Н	Н	Н	L	Forward, 2/5 times, SLOW
Н	L	Н	L	L	Н	L	Forward, 1/5 times, FAST
Н	L	Н	L	М	Н	L	Forward, 1/5 times, MIX
н	L	н	L	Н	Н	L	Forward, 1/5 times, SLOW
н	Н	Н	Н	L	L	н	Reverse, 2/5 times, FAST
Н	н	Н	Н	М	L	н	Reverse, 2/5 times, MIX
Н	н	Н	Н	Н	L	н	Reverse, 2/5 times, SLOW
н	Н	Н	L	L	L	н	Reverse, 1/5 times, FAST
Н	н	Н	L	М	L	н	Reverse, 1/5 times, MIX
н	н	н	L	Н	L	н	Reverse, 1/5 times, SLOW
L	н	Н	Н	L	L	L	Brake, 2/5 times, FAST
L	н	Н	Н	М	L	L	Brake, 2/5 times, MIX
L	н	Н	L	L	L	L	Brake, 1/5 times, FAST
L	н	Н	L	М	L	L	Brake, 1/5 times, MIX
L	н	Н	Х	Н	L	L	Brake, no current limiting
L	L	Н	Х	Х	OFF	OFF	Output OFF
Х	Х	L or OPEN	Х	Х	OFF	OFF	Standby mode (circuit OFF)

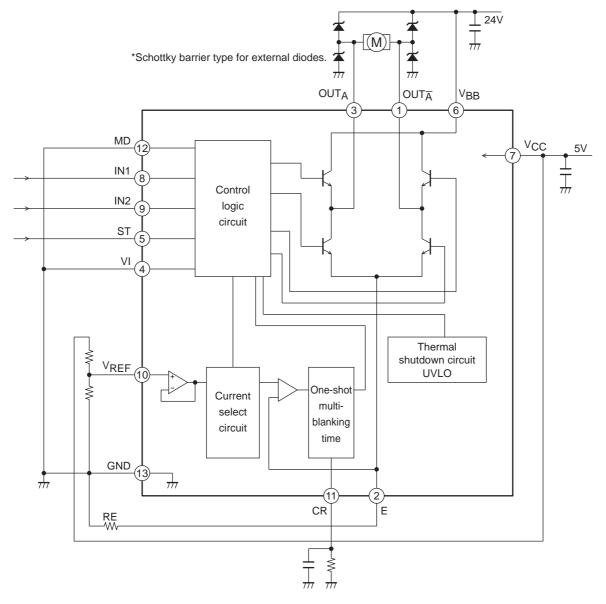
Except for MD pin, Low at input OPEN.

MD M: determined by external voltage.

Pin F	unction		
Pin No.	Pin name	Function	Equivalent circuit
1 3	OUT _A OUT A	Output pin.	
2	E	Sense voltage control pin.	
4	VI	High: sense voltage is 2/5 of V _{REF} Low: sense voltage is 1/5 of V _{REF}	
5	ST	High: circuit operation ON Low: standby mode	
8	IN1	High: rotation mode Low: brake mode	
9	IN2	High: reverse mode Low: forward mode	(4) ≰40kΩ VI ⁷⁷⁷
6	V _{BB}	Motor power supply voltage.	
7	V _{CC}	Logic power supply voltage.	
10	VREF	Output current setting reference pin. Setting range: 0 to (V _{CC} -2V)	<u>V_{CC}</u> 5μA
			40kΩ VREF 777
11	CR	Oscillator with self-excitation.	
12	MD	Current attenuation switching pin. Low : FAST DECAY High: SLOW DECAY M : MIX DECAY M is set by external power supply voltage. Range : 1.1 to 4.0V	
13	GND	Ground pin	

Sample Application Circuits

1. Forward/reverse motor with current limiter

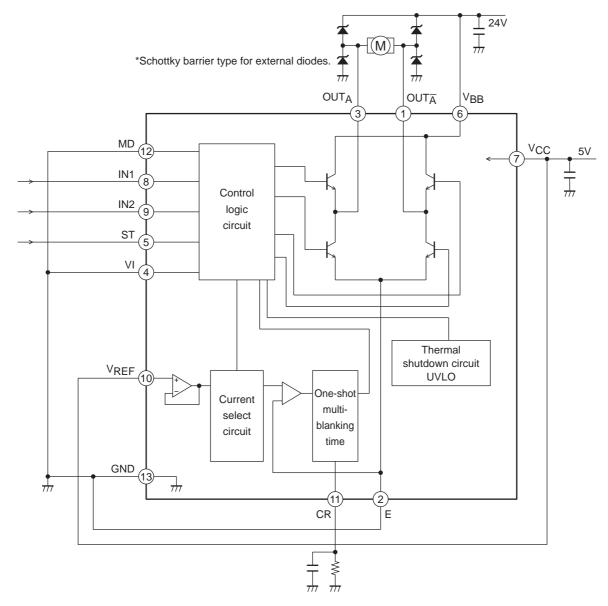


Limiter current setting method

 $I = V_{REF} / (5 \times RE)$

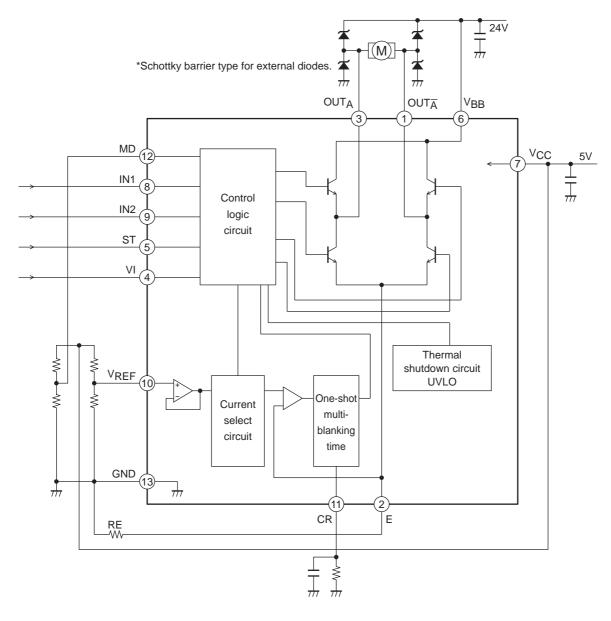
IN1	IN2	ST	OUT _A	OUTA	Mode
Н	L	Н	Н	L	Forward
Н	Н	Н	L	Н	Reverse
L	Н	н	L	L	Brake
L	L	н	OFF	OFF	Output OFF
_	-	L	OFF	OFF	Standby mode

2. Forward/reverse motor



IN1	IN2	ST	OUT _A	OUTA	Mode
Н	L	Н	Н	L	Forward
Н	Н	Н	L	Н	Reverse
L	Н	Н	L	L	Brake
L	L	Н	OFF	OFF	Output OFF
-	-	L	OFF	OFF	Standby mode

3. PWM current control forward/reverse motor (MIX DECAY)



Notes on Usage

1. VREF pin

Because the VREF pin serves for input of the set current reference voltage, precautions against noise must be taken.

2. GND pin

The ground circuit for this IC must be designed so as to allow for high-current switching. Blocks where high current flows must use low-impedance patterns and must be removed from small-signal lines. Especially the ground connection for the sensing resistor RE at pin E, and the ground connection for the Schottky barrier diodes should be in close proximity to the IC ground.

The capacitors between V_{CC} and ground, and V_{BB} and ground should be placed close to the V_{CC} and V_{BB} pins, respectively.

3. CR pin setting (Switching off time, Noise cancel time)

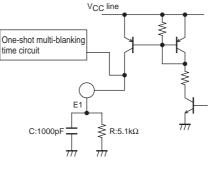
The noise cancel time (Tn) and the switching off time (Toff) are set by the following expressions:

Noise cancel time: Tn \approx C \times R \times ln {(1.0 - RI) / (4.0 - RI) [sec]

CR charge current: 1.3mA

Switching off time: Toff $\approx -C \times R \times \ln (1.0 / 4.8)$ [sec]

Internal configuration at CR pin



CR constant range: R = 4.7k to $100k\Omega$ C = 330pF to 2200pF

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